

**Payment Card Industry (PCI) Security Standards Council**  
**Letter of Approval**  
**PCI PIN Transaction Security Testing Program**

15 Dec 2023

George Jiang

**ID Tech**

10721 Walker Street  
 Cypress, CA 90630-4720  
 United States

PCI SSC PTS Approval Number:	<b>4-10245</b>	Approval Class:	<b>SCR</b>
Manufacturer:	<b>ID Tech</b>		
Name & Model Number:	<b>VP5300</b>		
Hardware Version Number:	<b>SPTP2-988-33-2C-0C; ID-80152002-00x (CTLS Antenna) , 80152001</b>		
Firmware Number:	<b>VP5300 FW v1.00.xxx.xxxx.S, VP5300 FW v1.01.xxx.xxxx.S, VP5300 FW v1.02.xxx.xxxx.S, VP5300 FW v1.03.xxx.xxxx.S</b>		
Application Version Number if applicable:			
PIN Support:	<b>Offline</b>		
Key Management TDES:	<b>N/A</b>		
Key Management AES:	<b>N/A</b>		
Prompt Control:	<b>N/A</b>		
PIN Entry Technology:	<b>N/A</b>		
Functions Provided:	<b>ICCR,MSR,CTLS,OP,SRED</b>		
Approved Components:	<b>ID Tech - SmartPIN L100, 4-10223 ID Tech - SmartPIN L80, 4-90149</b>		
<b>Approved to meet PCI Device Security Requirements POI 5.</b>			

Dear George Jiang:

PCI Security Standards Council, LLC ("PCI SSC") has received your request for PIN Transaction Security ("PTS") approval for the information identified above. In connection with your request, we have reviewed PTS Test Report number **23-LTR-179 v2.0 ID Tech VP5300 dPCI5.1.pdf**, which was generated by **SGS Brightsight BV**.

After assessing such file (including, but not limited to, the Report), PCI SSC has found reasonable evidence that the submitted sample(s) of the above-referenced PTS Device sufficiently conform to PCI SSC's PCI PTS Requirements, as specified in the *PCI PTS Manufacturer Self-Assessment*.

PCI SSC hereby (a) grants your PTS device approval, based on the requirements stated in the *PCI PIN Transaction Security Requirements* manual (which may be amended at any time and from time to time by PCI SSC), and (b) agrees to include your PTS device in *PCI SSC's Approved PIN Transaction Security Device List*. PCI SSC's grant to your PTS device model is subject and specifically incorporates (i) the General Terms and Conditions to the Letter of Approval enclosed as Exhibit A; and (ii) the terms and conditions of the Payment Card Industry *PIN Transaction Security Evaluation Testing Vendor Release Agreement* by and between PCI SSC and you. Because PCI SSC's grant is subject to such limitations, including certain events of termination, you and any third parties should confirm that such approval is current and has not been terminated by referring to the list of approved PTS devices published on the PCI SSC website, [www.pcisecuritystandards.org](http://www.pcisecuritystandards.org).

When granted, PCI SSC approval is provided by PCI SSC to ensure certain security and operational characteristics important to the achievement of PCI SSC's goals, but PCI SSC approval does not under any circumstances include any endorsement or warranty regarding the functionality, quality, or performance of any particular product or service. PCI SSC does not warrant any products or services provided by third parties. PCI SSC approval does not under any circumstances include or imply any product warranties from PCI SSC, including, without limitation, any implied warranties of merchantability, fitness for purpose, or non-infringement, all of which are expressly disclaimed by PCI SSC. All rights and remedies regarding products and services which have received PCI SSC approval, shall be provided by the party providing such products or services, and not by PCI SSC.

**This Letter of Approval is effective upon dispatch from PCI SSC, LLC.**

<b>Effective Date:</b>	<b>15 Dec 2023</b>
<b>Expiry Date:</b>	<b>30 Apr 2026</b>

PCI Security Standards Council, LLC

A handwritten signature in black ink, appearing to read 'Mark Mrotek', with a large loop on the left side and a long horizontal stroke extending to the right.

By:

Name: Mark Mrotek

Title: Director of Certification Programs, The PCI Security Standards Council

# Exhibit A: General Terms and Conditions to the Letter of Approval

PCI SSC's approval is based on the evaluation and testing performed by **SGS Brightsight BV** in **Netherlands**. PCI SSC's approval only applies to the PIN Transaction Security ("PTS") devices identical to the PTS device model evaluated and tested by **SGS Brightsight BV**, as indicated by the PTS device Identifier ("Identifier").

If any aspect of the PTS device is different from that which was evaluated and tested by **SGS Brightsight BV**, then the PTS device should not be considered approved by PCI SSC, nor promoted as approved, even if the PTS device conforms to the basic PTS model description contained in the approval letter.

Approval granted by PCI SSC does not supersede or relieve vendor from any additional testing requirements as may be imposed by national testing bodies, financial institutions, network services providers, or other customers. The manufacturer is encouraged to ensure that testing requirements from all relevant parties have been met and approvals granted prior to the sale or installation of the PTS device.

PCI SSC approval may be revoked at any time. Because this approval may be revoked at any time, no PCI SSC Member or other third party should rely on the approval letter at any time without first confirming the continued effectiveness of the approval with PCI SSC. PCI SSC reserves the right to modify the terms or duration of the approval at its sole discretion to accommodate business or security requirements. This Letter of Approval is subject to and hereby incorporates by reference the terms and conditions of the *Payment Card Industry PIN Transaction Security Evaluation Testing Vendor Release Agreement* by and between PCI SSC and the recipient of this Letter of Approval.

**Note:**

*All PTS devices submitted for testing must be identified such that a future purchaser can be certain of purchasing a PTS model that has successfully completed the evaluation process and has been approved by PCI SSC. The Identifier will be:*

- 1. Recognizable*
- 2. Used by Customers of the participating payment brands during the purchase process*
- 3. Used by PCI SSC and the testing laboratory to identify clearly the tested and approved the PTS device.*

*The components of the PTS Identifier include:*

- 1. Vendor Name*
- 2. Marketing Model Name/Number*
- 3. Hardware Version Number*
- 4. Firmware Version Number*
- 5. Application Number, if applicable*

*The PTS Identifier information will be included in the PCI SSC approval letter and on the PCI SSC website. If an identical PTS device is used across a family of devices, manufacturers are cautioned against using the PTS device Identifier information that may restrict approval only to the PTS model depicted.*

You may communicate to Customers<sup>1</sup> of the participating payment brands that PCI SSC has approved the PIN Transaction Security Device to be in compliance with PCI SSC's PIN Security Requirements provided, however, that:

- You also communicate any of the limitations on PCI SSC's approval described above under the heading Approval Process in the "PIN Transaction Security Device Testing and Approval Program Guide," and

- All written communications referring to PCI SSC approval shall contain the following legend:

"When granted, PCI SSC approval is provided by PCI SSC to ensure certain security and operational characteristics important to the achievement of PCI SSC's goals, but PCI SSC approval does not under any circumstances include any endorsement or warranty regarding the functionality, quality or performance of any particular product or service. PCI SSC does not warrant any products or services provided by third parties. PCI SSC approval does not under any circumstances include or imply any product warranties from PCI SSC, including, without limitation, any implied warranties of **merchantability, fitness for purpose, or non-infringement**, all of which are expressly disclaimed by PCI SSC. All rights and remedies regarding products and services which have received PCI SSC approval shall be provided by the party providing such products or services, and not by PCI SSC."

<sup>1</sup> See PCI PTS Device Testing and Approval Guide for more information